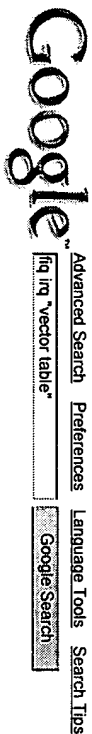


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## DAG Project: DAG Boot and Monitor

... is a jump around the interrupt vector table to location ... ARM IRQ exception (vector=0x18)  
not applicable, not ... ARM FIQ exception (vector=0x1c), not applicable, not ...  
dag.cs.waikato.ac.nz/dag/day-monitor.html - 11k - Cached - Similar pages

Samsung S3C2410 ARM920T bootcode written by PALADIN

... b HandlerIRQ ;handler for IRQ interrupt 0x18 ... HandlerFIQ ;handler for FIQ interrupt 0x1C ... bne LOOP ;=== Exception Vector Table Setup ...

[www.x-x.jp/embedded/dev\\_notes/Z410init\\_030701.htm](http://www.x-x.jp/embedded/dev_notes/Z410init_030701.htm) - 16k - Cached - Similar pages

```
... <endgroup> </endgroup> #endif: take /' jump vector table as in ... b software_interrupt
b abort; gotoch b abort; _data b nol; used b irq b irq reset; //isgn ...
opernam linuxfrom.net/doc/Btoolcode/start.s - 26k - Cached - Similar pages
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... After it comes back from STARTUPCODE the vector table is copied from rom to ram ... table base address (physical) and whether angle uses the Fiq or IRQ interrupt ...

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... bit 6 (0 is least significant bit) disables the FIQ. ... bit 7 (0 is least significant bit) disables the IRQ. ... 0 (bytes 0-3) of the exception vector table (EVT). ...  
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 ... **FIQ** Setting bit 6 (0) is least significant bit disables the **FIQ** ... Setting bit 7 (0  
 is least significant bit) disables the **IRQ** ... of the exception vector table (EVT ...  
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5.3 Memory Setup ...  
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 \*\*\* NWDQDF TMS TDO TDI TCK MCKI P25MCKL P12/F1Q P91/R00 P10 ... 5 Eight levels of priority  
 a Hardware vector table 0x00000000 0x00000018 Core IRQ Vector Idr ...  
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Samsung\_S3C2410\_ARM920T\_bootcode\_written\_by\_PALADIN

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... &amp;quot;location:&amp;quot;.&endrl;.&text / Jump vector table as in ... b software\_interrupt b abort\_pretetch b abort\_data b not\_used b irq\_b fiq\_reset; /fsign ...  
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... After it comes back from STARTUPCODE the vector table is copied from rom to ram ... table base address (physical) and whether angel uses the FIQ or IRQ interrupt ...  
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